Intelligent Test Generation for Functional Verification with Infineon UK

**Type of award**  PhD Research Studentship

**Department**  Computer Science

**Scholarship Details**  Scholarship covers full UK/EU (EU applicants who have been resident in the UK for 3 years prior to application) PhD tuition fees and a tax-free stipend at the RCUK rate (£15,009 in 2019/20), enhanced by an additional industrial top-up subject to contracts. EU nationals resident in the EU may also apply but will qualify only for PhD tuition fees

**Duration**  4 years

**Eligibility**  Home/EU applicants only

**Deadline**  1 October 2019

**PhD Topic Background/Description**

Functional verification ensures the correct functioning of complex semiconductors. Companies invest a great deal of money and effort in functional verification for various reasons – to ensure a good user experience, to comply with regulators, to avoid the considerable costs of recalls and litigation and, in safety critical applications, to prevent the loss of life.

The two state-of-the-art approaches to functional verification are constrained-random verification and formal verification. The former is inefficient, with many simulation cycles spent exploring the same state space in much the same way; guiding the tool into the interesting corner cases present in complex systems typically requires considerable input from engineers. On the other hand, formal verification can find corner cases with little manual steering, but complexity limits mean that it can only be applied exhaustively to relatively small blocks.

We aim to use advanced learning to direct the generation of stimuli so that the interesting corner cases on a large complex design can be reached in an automated way. In this project we intend to employ machine learning techniques to help find correlations between observed coverage and stimulus. In particular, we will investigate statistical techniques capable of uncertainty propagation to predict the likelihood of given stimulus to achieve verification goals. This information is expected to help direct stimulus generation in a series of iterative cycles, each refining the information available to the learner. The programme of research includes training the test generator to stay within a set of constraints, so it produces valid stimulus; training the test generator to trigger checkers in the design; and training the test generator to achieve coverage goals. Our objective is to generate otherwise hard to find input sequences and to identify complex DUT configurations. We expect that this significantly improves the level of automation and effectiveness of test generation.
Further Particulars

Candidate Requirements
We are looking for an enthusiastic student with at least a 2.1 or first class degree in Computer Science, Computer Systems Engineering, Informatics, Microelectronic Design or a similar discipline.

Excellent programming skills are essential. A good understanding of computer architecture and machine learning techniques will be required for the project. It would be an advantage to have a background in at least one of the following areas: machine learning, processor architecture and design, simulation-based testing, model-based design or testing techniques.

You are able to quickly pick up new programming languages and you are willing to learn how to use state-of-the-art professional EDA tools for design verification. You are a competent presenter, writer and communicator, willing and able to work with our industrial collaborator.

Scholarship Details
Research Council £15,009 p.a. in 2019/20 plus an industrial top-up of up to £5,000 depending on age and experience and subject to contracts.

Informal enquiries
Please email Prof Kerstin Eder (Kerstin.Eder@bristol.ac.uk)
For general enquiries, please email seem-pgr-admissions@bristol.ac.uk

Application Details
To apply for this studentship submit a PhD application using our online application system [www.bristol.ac.uk/pg-howtoapply]

Please ensure that in the Funding section you tick “I would like to be considered for a funding award from the Computer Science Department” and specify the title of the scholarship in the “other” box below with the name of the supervisor.

Closing date for applications 31 July 2019.

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